

Appn No. 10/815,632  
Amdt. Dated June 9, 2006  
Response to Office Action of May 16, 2006

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**Amendments to the Specification:**

**The paragraph beginning at Page 104, lines 28-31, is to be amended as follows:**

The imaging unit incorporates both the image sensor 2412 and the image processor 2410, which are usefully combined into a single compact chip as described in the co-pending US application applications USSN 10/778,056 entitled "Image Sensor with Digital Framestore"; USSN 10/778,058 entitled "Image Sensor with Low Pass Filter"; USSN 10/778,060 entitled "Image Sensor with Range Expander"; USSN 10/778,059 entitled "Pixel Sensor"; USSN 10/778,063 entitled "Image Sensor Timing Circuit"; USSN 10/778,062 entitled "Image Processor With Low Power Mode"; USSN 10/778,061 entitled "Image Processor"; USSN 10/778,057 entitled "Synchronization Protocol" (docket no. NPS047-US - NPS054) all filed 17 February 2004.